

Frequency response of Diamond-like Nanocomposite thin film based MIM capacitor and equivalent circuit modelling

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Abstract: The frequency response of metal-insulator-metal (MIM) based thin film capacitors were studied using LCR bridge where diamond-like nanocomposite (DLN) film behaves as a dielectric medium. The films were deposited by plasma assisted chemical vapour deposition (PACVD) method. Fourier transforms infrared spectroscopy (FTIR) and Raman spectroscopy give the structure of DLN film. The results show that, equivalent parallel capacitance (EPC) decreases sharply beyond 10^5 Hz for thinner films. But for thicker films, there is no such decrease. This is due to some parasitic series resistance effect in the capacitor circuit. An equivalent circuit model for real capacitor has been established. Moreover, there is also a small decrement in EPC with frequency and this effect increases with thickness of film. This may be due to lack of sufficient time for electron transportation through bulk DLN material. The DLN based thin film capacitor has a great potential for use in electronic/electrical system.

Keywords: DLN, PACVD, frequency response capacitance, thin film capacitor, etc.

I. Introduction

Diamond-like Nanocomposite, an amorphous material comprises of two interpenetrating network structures. These are one diamond like carbon bonds network stabilized by hydrogen (a-C:H) and another quartz like silicon network stabilized by oxygen (a-Si:O) [1-4]. Different chemical bonds between constituent atoms of the two networks lead to low residual stress [8]. Apart from different tribological [3, 4, 6], thermal [4, 5], chemical [10], biocompatibility [7] properties, DLN has also good dielectric properties [1, 2]. DLN possess electrical resistivity of 10^8 to 10^{14} ohm cm, dielectric constant 3 to 10 and breakdown strength in the range of 10^6 to 10^8 V/cm [1, 2, 9]. Capacitor has been fabricated with DLN thin film as the dielectric medium sandwiched by two metal electrodes. Measurement of frequency response of capacitor fabricated by us in the frequency range of 10^2 Hz to 10^6 Hz has been reported. An equivalent circuit that represents the real behaviour of the capacitor in electric circuit has also been modelled.

II. Experimental

2.1. Synthesis of DLN film:

Diamond-like Nanocomposite coatings were deposited on aluminium coated silicon substrate by plasma assisted chemical vapour deposition (PACVD) process. The base pressure is 2×10^{-5} mbar and working pressure is 3×10^{-4} mbar. Substrates are cleaned by conventional method and again cleaned in situ by plasma etching prior to deposition. A liquid siloxane precursor injected in the low pressure chamber vapourises with a hot filament and in turn ionizes by bombarding thermionic electrons from filament to generate the plasma atmosphere of precursor elements. The filament current is in the range of 90-110A. The ions are then pulled towards a rotating substrate holder by using a high frequency RF self-bias of -550V. During deposition process, argon gas flows in to the chamber at the rate of 50ml/min.

2.2: Fabrication of DLN based capacitor:

Silicon is cleaned by conventional cleaning method and the aluminium thin film is deposited on silicon substrate by vacuum evaporation (PVD) method. Then the DLN film is deposited on aluminium coated silicon substrate by PACVD method. Finally, the aluminum thin film is again deposited on DLN surface. The contact points are achieved by using silver pest. A typical structure of the capacitor is shown in Fig. 1.

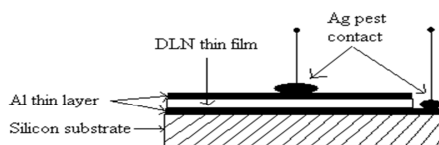


Figure.1. Schematic diagram of single layer DLN based thin film capacitor

III. Results and discussion

In order to study of structure of DLN film, the FTIR spectrum of a representative film was recorded from 400 -4000 cm^{-1} range. The corresponding FTIR trace has been shown in Fig.2. The FTIR trace of the film shows two networks: one is Diamond-Like C:H network and another is Si:O network and they are interpenetrated with the Si-C bonding depicting the typical nature of DLN film [3].

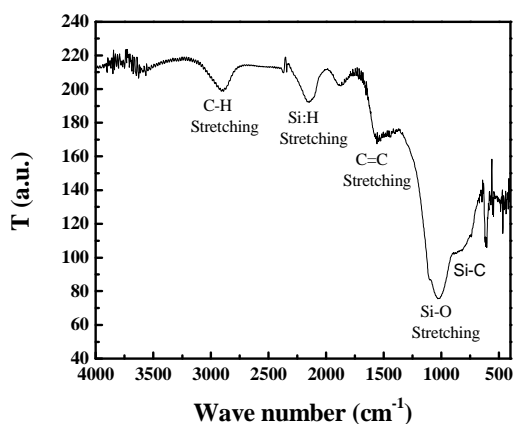


Figure. 2. The FTIR spectra of DLN film shows the bonding structure.

Raman spectrum was investigated in the wave number ranging from 1100 to 1800 cm^{-1} . Raman spectrum analysis of DLN film is deconvoluted into two Gaussian peaks. The G-peak is due to C=C sp^2 stretching vibration and the D-peak is attributed to the disordered breathing motion of six-fold aromatic rings [3, 5]. Thus, the peak positions in the Raman spectrum of DLN film and the intensity ratio of D/G-peak are the most important parameters to understand the electrical properties of the DLN film because sp^2 π bonds are responsible for electron transportation through DLN film. There are two bands around 1355 cm^{-1} (D-peak) and 1524 cm^{-1} (G-peak) shown in Fig.3. The I_D/I_G ratio is observed to be 0.35 which may be indirectly related with sp^3/sp^2 ratio [11].

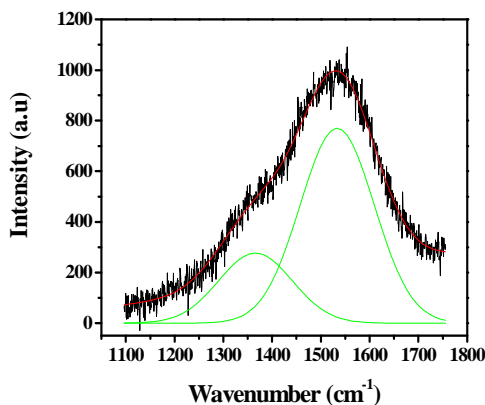


Figure.3. Raman spectra of DLN film is deconvoluted into two Gaussian peaks.

3.1. Frequency response of EPC:

The frequency response of EPC of DLN based thin film MIM capacitor has been recorded using Agilent 4284A Precision LCR Meter at room temperature. The peak to peak voltage of ac signal was 0.5V with frequency ranges from 10^2 Hz to 10^6 Hz. The Frequency response of three single layer DLN thin films based MIM capacitors has been shown in Fig. 5 and Fig.6. The thickness of DLN films are 118nm, 313nm and 1090nm respectively. It is observed that the EPC is almost constant up to 10^5 Hz of ac source then decreases sharply for both of thin capacitors. This may be due to some parasitic series resistance effect. We applied an equivalent model for real capacitor to explain such behaviour in section 3.2. Moreover, a small gradient of EPC with frequency is present in all capacitors and the rate of such decrement increases with thickness of film. This may be due to lack of sufficient time to transport of charge through bulk DLN film.

3.2 Equivalent capacitor model:

In real capacitor, there must be a definite leakage resistance in between two electrodes and a small series resistance due to resistance of thin metal electrodes and contact terminals. These resistances are termed as parasitic resistance. The LCR bridge measured the impedance by assuming a parallel combination of capacitor and resistance i.e. equivalent parallel capacitance (C_p) and equivalent parallel resistance (R_p).

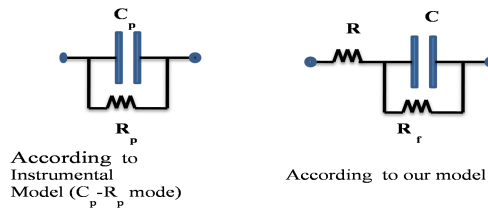


Figure 4: Diagram of instrumental model circuit and equivalent model circuit as considered.

According to the instrumental set up, total impedance measured by LCR bridge is converted in two impedances: one is due to equivalent parallel capacitance (C_p) and another is due to equivalent parallel resistance (R_p). According to our model (Fig. 4), we assumed that an ideal capacitor (C) which is independent of frequency (\sim MHz), a parallel leakage resistance (R_f) and parasitic resistance (R_s) in series.

Total impedance (Z_{ex}) according to instrumental set up is divided as a real part and imaginary part. Similarly, total impedance of equivalent model circuit is divided in real and imaginary parts. Comparing the real and imaginary part of equivalent model circuit and instrumental circuit, we get,

$$C_p = \frac{CR_f^2}{(xR_s^2 + 2R_sR_f + R_f^2)} \dots\dots\dots 1$$

$$R_p = \frac{(xR_s^2 + 2R_sR_f + R_f^2)}{(xR_s + R_f)} \dots\dots\dots 2$$

$$\text{where } x = 1 + \omega^2 C^2 R_f^2 \dots\dots\dots 3$$

In general, the series resistance (R_s) is much smaller than the leakage resistance (R_f). At low frequency, 'x' in equation 1 is close to one hence; C_p and R_p are nearly equal to C and R_f respectively according to equations 1 and 2. If R_f is very high (\sim M Ω or higher), then R_s has no significant effect on EPC even at high frequency (10^6 Hz). But if R_f is low (\sim k Ω), then R_s can play an important role on EPC at higher frequency. By changing the value of R_s , we fitted the experimental graph from the equivalent model circuit.

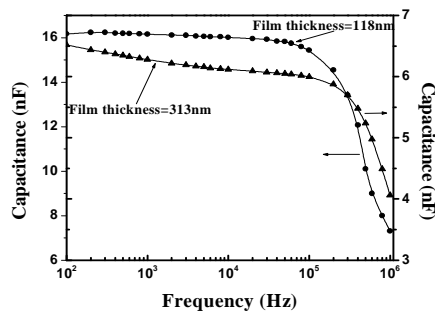


Figure 5: Frequency response of EPC for 118nm and 313nm thick DLN based capacitor.

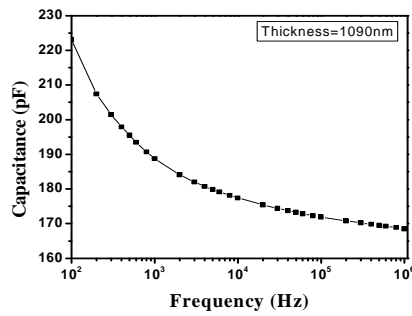


Figure 6: Frequency response of EPC for 1090 nm thick DLN based capacitor.

The frequency response of EPC of single layer (thickness=118nm) DLN film capacitor has good agreement with equivalent model circuit (Fig. 7). The EPC and EPR of the capacitor are 16.2nF and 9.48kΩ respectively at frequency 100Hz. So the value of C and R_f are 16.2nF and 9.48kΩ respectively.

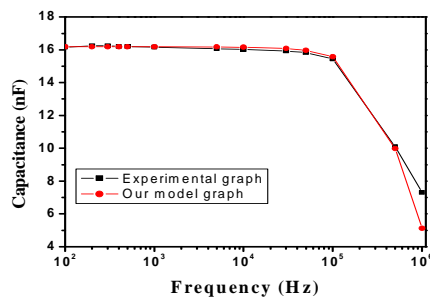


Figure. 7: Capacitance vs. frequency response for DLN single layer thin film capacitor of thickness 118nm. ($R_s = 15\Omega$, $R_f = 9.48k\Omega$, $C = 16.2nF$)

It has been seen by trial and error method that, R_s is equal to 15Ω for better fitment of experimental results.

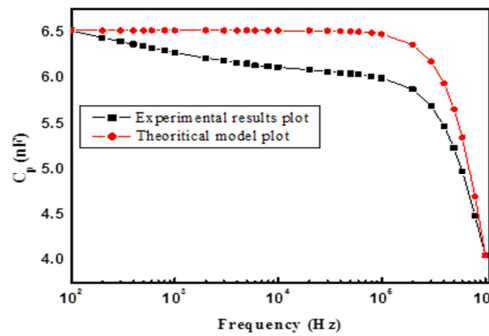


Figure 8: Capacitance vs. frequency response for DLN single layer thin film capacitor of thickness 313nm. ($R_s = 18\Omega$, $R_f = 4.5 M\Omega$, $C = 6.52nF$).

Fig. 8 also shows that the frequency response of EPC of single layer (thickness=313nm) DLN film capacitor has agreement with theoretical model. The EPC and EPR of the capacitor are 6.52nF and 4.5MΩ respectively at frequency 100Hz. Similarly, the value of C and R_f are 6.52nF and 4.5MΩ respectively. It also has been seen by trial and error method that, R_s is equal to 18Ω for better fitment of experimental results. There is no sharp decrement of EPC at high frequency for thick layer DLN film (1090 nm) which shown in Fig. 6. Because the leakage resistant is very high comparable to series resistance and hence does not have any effect within frequency 1 MHz on EPC according to equation 1. The EPC and EPR are 223pF and 30MΩ respectively at 100Hz frequency.

Another interesting observation is that, there is a gradual decrease of EPC with frequency up to 10^5 Hz. This effect increases with film thickness. These may be explained as follows: for any definite potential difference across the capacitor the field strength becomes very high for very thin dielectric film. Due to the high field strength and defect states present in the bulk of the film, transportation of electrons through DLN film may be explained by Pool-Frankle model for insulators. The transportation causes electron drift velocity, so electrons

take some time to cross the bulk material if applied field remains active in that direction. For alternating electric field, as applied field frequency increases frequent change in field direction causes decrease in transportation of electrons because of lesser available time for them to be transported. Hence current decreases in the circuit i.e. circuit impedance increases. Increase of impedance signifies decrease of EPC. Apart from this when thickness of DLN film increases the field strength decreases for specific potential difference and the path length for electrons also increase as a result time for transportation further increase causing lowering of EPC. When the frequency is quite high $>10^4$ Hz the decrement in EPC reduces causing more or less a steady capacitance for thicker film (Fig. 9).

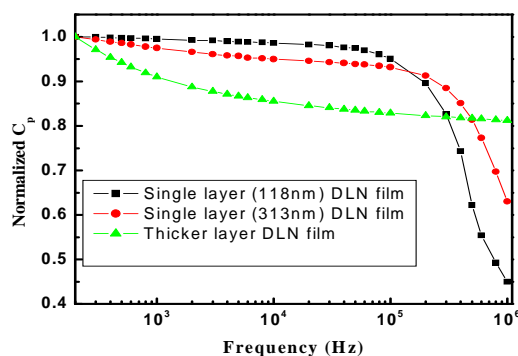


Figure 9. Normalized EPC for four different samples

The sharp decrease in EPC after 10^5 Hz for thinner DLN film layered capacitors in Fig 9 may be attributed to parasitic series resistance effect in the capacitor circuit as mentioned in section 3.2.

IV. Conclusion

The study for capacitance against frequency of DLN thin film based MIM capacitor shows that, parasitic resistance has a great effect on EPC at high frequency operation. To avoid this effect, the leakage resistance should be very high and parasitic resistance should be very low. For a particular dielectric medium to increase the leakage resistance the thickness of dielectric must be enlarged which leads to decrease in capacitance for fixed electrode area. In case of high capacitance and high frequency operation, our model can be helpful to design an electrical/ electronic circuit properly since it has good matching with experimental studies. Hence, DLN thin film based MIM capacitor has great potential for use in electrical/ electronic circuit.

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